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TECHNOLOGY CENTER 2800  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. ....  
Filing Date .....  
Inventor .....  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2827  
Examiner ..... David A. Zarneke  
Attorney's Docket No. .... MI22-1572  
Title: Methods of Forming Board-On-Chip Packages

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449, a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 12-26-02

By: D. Brent Kenady  
D. Brent Kenady  
Reg. No. 40,045

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